

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

# U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

APPLICATION NO.  
09/756686

CONT/PRIOR  
F

CLASS  
257

SUBCLASS  
339

ART UNIT  
2811

EXAMINER

Loke

TITLE

APPLICANTS  
Kazuo Matsuzaki  
Naoto Fujishima  
Akio Kitamura  
Gen Tada

Semiconductor device exhibiting a high breakdown voltage and the method of manufacturing the same

PTO-2040  
12/99

## ISSUING CLASSIFICATION

CROSS REFERENCE

CLASS

SUBCLASS (ONE SUBCLASS PER BLOCK)

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL  
DISCLAIMER

DRAWINGS

CLAIMS ALLOWED

Sheets Dwg.

Figs. Dwg.

Print Fig.

Total Claims

Print Claim for O.G.

☐ The term of this patent  
subsequent to \_\_\_\_\_ (date)  
has been disclaimed.

☐ The term of this patent shall  
not extend beyond the expiration date  
of U.S. Patent No. \_\_\_\_\_

☐ The terminal \_\_\_\_\_ months of  
this patent have been disclaimed.

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date)

(Legal Instruments Examiner)

(Date)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

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(FACE)